

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re the Application of:

Tsuyoshi TOMITA

Application No.: 09/263,766

Filed: March 5, 1999

For: SIGNAL PROCESSING CIRCUIT FOR PREVENTING PSEUDO LOCK OF TIMING
RECOVERY PLL



Group Art Unit: 2631

Examiner: Burd, Kevin M.

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Date: July 8, 2003

Sir:

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